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1	42012	honda.in. OR kanzawa.in. OR moriyama.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 07:40
2	✓ 11	(honda.in. OR kanzawa.in. OR moriyama.in.) AND ((bidirection\$3 ADJ1 line ADJ1 switch\$3) OR BLSR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 07:42
4	✓ 14	(honda.in. OR kanzawa.in. OR moriyama.in.) AND ((automatic\$4 ADJ1 protect\$5 ADJ1 switch\$3) OR APS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 07:44
7	✗ ✓ 375	((bi\$1direction\$5 ADJ1 line ADJ1 switch\$3) OR BLSR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 15:33
12	5076	(span ADJ1 switch\$5) OR span\$2switch\$5 OR (ring ADJ1 switch\$5) OR ring\$1switch\$5 OR SF\$1S OR SF\$1R	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 10:57
13	2274669	ring\$3 OR loop\$3 OR SONET\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 11:02
16	1416959	fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 11:00
17	502	((span ADJ1 switch\$5) OR span\$2switch\$5 OR (ring ADJ1 switch\$5) OR ring\$1switch\$5 OR SF\$1S OR SF\$1R) SAME (fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 10:02
18	✓ 310	((span ADJ1 switch\$5) OR span\$2switch\$5 OR (ring ADJ1 switch\$5) OR ring\$1switch\$5 OR SF\$1S OR SF\$1R) SAME (fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5)) AND (ring\$3 OR loop\$3 OR SONET\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 10:03
22	✓ 98	((span ADJ1 switch\$5) OR span\$2switch\$5 OR SF\$1S) SAME ((ring ADJ1 switch\$5) OR ring\$1switch\$5 OR SF\$1R)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 10:58
24	✓ 31	GR-1230\$1CORE\$4 OR (GR ADJ1 "1230" ADJ1 CORE\$4) OR (GR-1230 ADJ1 CORE\$4) OR GR-1230-CORE OR R6-151 OR (R6 ADJ1 "151")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 10:59
29	85779	work\$5 SAME protect\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 12:10
30	4180	(ring\$3 OR loop\$3 OR SONET\$1) SAME (work\$5 SAME protect\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 11:03
31	508	(ring\$3 OR loop\$3 OR SONET\$1) SAME (work\$5 SAME protect\$5) SAME (fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 11:03

32	✓ 280	((ring\$3 OR loop\$3 OR SONET\$1) SAME (work\$5 SAME protect\$5) SAME (fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5)) AND (370/\$6.cccls. OR 714/\$6.cccls. OR 359/\$6.cccls. OR 398/\$6.cccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 11:03
37	2076	370/216,217,221-224,225,228.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:17
40	✓ 89	370/216,217,221-224,225,228.cccls. AND ((span ADJ1 switch\$5) OR span\$2switch\$5 OR (ring ADJ1 switch\$5) OR ring\$1switch\$5 OR SF\$1S OR SF\$1R)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 11:53
48	✓ 357	370/216,225,228.cccls. AND ((ring\$3 OR loop\$3 OR SONET\$1) SAME (fault\$3 OR fail\$5 OR SD OR (signal ADJ1 degrad\$5) OR SF OR (signal ADJ1 fail\$5)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:09
52	589	370/228.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:09
53	✓ 239	370/228.cccls. AND (ring\$3 OR loop\$3 OR SONET\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:09
56	14105	(automatic ADJ1 protect\$5 ADJ1 switch\$3) OR APS OR (K\$2 ADJ1 byte\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:12
57	173	((automatic ADJ1 protect\$5 ADJ1 switch\$3) OR APS) OR (K\$2 ADJ1 byte\$1)) SAME (work\$5 SAME protect\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:16
62	533	((automatic ADJ1 protect\$5 ADJ1 switch\$3) OR APS) OR (K\$2 ADJ1 byte\$1)) AND (work\$5 SAME protect\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:30
63	123	370/216-228.cccls. AND (((automatic ADJ1 protect\$5 ADJ1 switch\$3) OR APS) OR (K\$2 ADJ1 byte\$1)) AND (work\$5 SAME protect\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:17
85	✓ 2	6269452.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:12
88	✓ 12	("4847610"   "5319633"   "5341364"   "5442620"   "5469428"   "5550805"   "5663950"   "5712968"   "5737310"   "5949755"   "6269452"   "RE37401").PN.	USPAT	2004/01/09 15:02
89	✓ 3	6430700.URPN.	USPAT	2004/01/09 15:03
92	✓ 10	("4847610"   "5319633"   "5341364"   "5442620"   "5469428"   "5550805"   "5663950"   "5712968"   "5737310"   "5949755").PN.	USPAT	2004/01/09 15:04
93	✓ 6	6269452.URPN.	USPAT	2004/01/09 15:06
55	14105	((automatic ADJ1 protect\$5 ADJ1 switch\$3) OR APS) OR (K\$2 ADJ1 byte\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 12:12

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## RFC2892

... [2] IEEE 802.5 Token Ring Specification. [3] Bellcore GR-1230, Issue 4, Dec. 1998, "SONET Bidirectional Line-Switched Ring Equipment Generic Criteria". ...

[www.scit.wlv.ac.uk/rfc/rfc28xx/RFC2892.html](http://www.scit.wlv.ac.uk/rfc/rfc28xx/RFC2892.html) - 75k - [Cached](#) - [Similar pages](#)

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... **GR-1230-CORE**, "SONET Bi-directional Line Switched Ring (BLSR) Equipment Generic Criteria," **Issue 4**, (Bellcore, December 1998). ...  
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... 1996. [GR1230] **GR-1230-CORE**, SONET Bi-directional Line-Switched Ring Equipment Generic Criteria, **Issue 4**, December 1998. [GR3009 ...  
[www.watersprings.org/links/mlr/id/draft-guo-optical-mesh-ring-01.txt](http://www.watersprings.org/links/mlr/id/draft-guo-optical-mesh-ring-01.txt) - 26k - [Cached](#) - [Similar pages](#)

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... **GR-1230-Core** (Bidirectional Line Switched Rings [**BLSR**]); GR-1244-Core (Network Synchronization); GR-1400-Core (Unidirectional Path Switched Rings [**UPSR**]). ...

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... no requirements for inservice upgrade from linear-APS to **BLSR**, but there is an objective in Telcordia SONET **BLSR** functional requirements document **GR-1230-CORE**: ...

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... In a 2-fiber **BLSR**, half of the bandwidth on each of the two fibers is reserved for protection. **GR-1230-CORE**[] provides generic criteria for both 2- and 4-fiber ...

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... is a self-healing type ring network that adopts the **BLSR**(Bi-Directional Line Switched

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**2 Availability model of bidirectional line switched ring**

*Rados, I.; Turalija, P.; Sunaric, T.;*

Transparent Optical Networks, 2001. Proceedings of 2001 3rd International Conference on , 18-21 June 2001

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**3 Grooming of arbitrary traffic in SONET/WDM BLSRs**

*Peng-Jun Wan; Calinescu, G.; Frieder, O.;*

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**4 Practical traffic grooming scheme for single-hub SONET/WDM rings**

*Xiang-Yang Li; Liwu Liu; Peng-Jun Wan; Frieder, O.;*

Communication Technology Proceedings, 2000. WCC - ICCT 2000. International Conference on , Volume: 2 , 21-25 Aug. 2000

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<near/1> "1230" <near/1>
core*) OR r6*151 or (r6
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